Dr.A.S.V.VIJAYA LAKSHMI

Residential Address:
Flat No:FF-10,
Meghana Grand Exotica,
P.M.Palem,
Visakhapatnam-530048

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OBJECTIVE:

To acquire a challenging and creative position in an esteemed organization by seeking exposure to latest technologies while making good contributions for further career development.

ACADEMIC QUALIFICATIONS:

S. No	Qualification	Board/University	College/School	Year of passing
1	PhD	JNTU Kakinada	JNTU Kakinada	2022
2	M.Tech (PSCA)	JNTU Kakinada	GVP College of Engineering,Visakapatnam	2010
3	B.Tech (EEE)	JNTU Kakinada	JNTU College of Engineering (Autonomous) kakinada	2002
4	Intermediate (M.P.C)	Board of Intermediate Education , AP,Hyderabad	Nalanda Girls Junior College, Vijayawada	1998
5	S.S.C	Board Of Secondary Education, AndhraPradesh	RLY HIGH SCHOOL Guntupalli, Krishna Dt	1996

SKILL SETS:

Programming languages :	Matlab/Simulink
Operating systems :	MS-DOS, WINDOWS-98/XP/7/8/10

TEACHING EXPERIENCE:

S. No	Organization	Exp type	Experience	Designation	Experience	
					Years	Months
1	Avanthi College of Engineering,	Academic	June-2005 to Nov 2007	Assistant Professor	2	6

	Narasipatnam					
2	GVP College of Engineering for Women	Academic	1/7/2009 – till date	Assistant Professor	13	03

ACADEMIC PROJECT DETAILS

Title of the PhD Thesis: Development and design of optimal robust PID-PSS for widespread operated Power System based on simplified interval approach stability conditions.

Area of Research: Control aspects of Power System.

Tool Used : Matlab/Simulink

M.TECH Project

Title : A Fast Hopfield Neural Network approach for online Economic Dispatch of Power System

Team Size : 1 member

Tool Used : Matlab

B.TECH Project

Title : An inward approach in the design of control systems

Team Size : 4 members

Tool Used : Matlab.

SUBJECTS TAUGHT AT UNDERGRADUATE LEVEL

FACTS	Power System Operation and Control
Power Systems-I	Power Systems-II
Electrical machines-III	Electrical Machines-I
Electrical Distribution Systems	Power System Analysis
Control Systems	Electro Magnetic Field Theory
Electrical Technology	Network Analysis
Power Electronics	Power Semi Conductor Drives
UEE	Basic electrical Engineering

SHORT TERM COURSES/FDP's ATTENDED:

1. A Two week FDP on **"Neural Networks ,Fuzzy Systems & their Applications to Electrical Engineering",** organized by department of EEE GVP College of Engineering Visakhapatnam, during **4th-16th November-2013.**

2. A three day Staff development programme on "***Electromagnetic and RF Fundamentals"*,**organized by GVP College of Engineering for Women,**22nd to 24th January-2016.**
3. A short-term course on "**Cyber Physical Systems**" held at **IIT-Kanpur**,India during **20-24 March-2017**.
4. A one week FDP on **"Introduction to SmartGrid"** conducted by **NPTEL-AICTE** during **August-September 2018**
5. One week short term course training program on "**Restructured Power System Modelling and Simulation",** from 26th to 31st August 2019 held at department of EEE,ANITS.
6. One week unique hands on International online FDP on "**Control Systems Design –From A Begineer To An Expert-1.0**", Organized by EEE department GMR Institute of Technology,Rajam, 25th-30th May,2020.
7. One Week online National FDP on "**APPLICATIONS OF OPTIMIZATION TECHNIQUES TO ELECTRICAL ENGINEERING**" organized by Department of Electrical and Electronics Engineering, Gayatri Vidya Parishad College of Engineering for Women, Visakhapatnam during 22nd to 26th June 2020.
8. A Short term training program on "**Automation and Intelligent Control of Electrical Systems -Phase-I**" organized by Department of Electrical and Electronics Engineering, Gudlavalleru Engineering College during 3rd to 8th August 2020.
9. One Week Short term Course on **"Advances in control system Engineering and Applications",** organized by department of EEE Sradhar Vallabhbhai NIT Surat during 23rd -27th September 2020.
10. A Short term training program on "**Automation and Intelligent Control of Electrical Systems -Phase-III**" organized by Department of Electrical and Electronics Engineering, Gudlavalleru Engineering College during 16th to 21st November 2020.
11. A Short term training program on "**Automation and Intelligent Control of Electrical Systems -Phase-IV**" organized by Department of Electrical and Electronics Engineering, Gudlavalleru Engineering College during 15th to 20th March 2021.

WORKSHOP

1. A three day workshop on " ***Adavanced Digital Signal Processing and Applications***", during **2th -4th July 2011**,Oraganised by IIPC, G.V. P. E. W, Visakhapatnam.
2. A two day National Level workshop on "***Wide Area Monitoring and Control of Sustainable Power Systems***", organized by GITAM UNIVERSITY,Visakhapatnam,**24th -25th January,2013.**
3. A two day International Workshop on "**Trends in Power System protection and Control",** organized by JNTUK, University College of Engineering Vizianagaram,**19-20th July 2013**.
4. Attended a **Series of technical** talks by Mr David B.Durocher,President IAS,Dr Peter Magyar,and Dr ,Sastry V Vedula conducted in association with **IEEE on 13th April 2015** at GVP College of Engineering, Visakhapatnam
5. A two day National Workshop on " ***Plannig ,Operation and Control of Microgrid-1"*** organized by EEE department of Andhra University,**26-27 November,2015.**

6. A three day Workshop on **"Lab View-Multisim exposurethrough Integrated Electrical machines& Drive Systems laboratory",** organized by EEE&ECE department GVP College of Engineering, Visakhapatnam **during 24th to 26th June 2016.**
7. A two day international Workshop on "**Cyber Physical Systems**" held at **IIT-Kanpur**,India during **25-26 March-2017**
8. A three day national workshop on **"Biological Inspired Computing & Applications(BICA-2018)",** organized by CSE GVP College of Engineering for Women during **9th -11th March 2018**.
9. A two day hands on workshop on **"Metaheuristic Optimization in Multidispciplinary Reasearch(MOMR-2018)",** Organized by EEE department AU College of Engineering(A),Andhra University during 26th-27th October-2018.

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ONLINE CERTIFICATION COURSES

1. A course on **"Introduction to smart grid"** conducted by **NPTEL-AICTE** during **August-September 2018**.
2. A course on **"Power System Engineering"** conducted by **NPTEL-AICTE** during **Janurary-April 2019**.

PUBLICATIONS

1. Vijaya Lakshmi, A.S.V., Ramalinga Raju M., & Siva Kumar M.(2020). **Design of a robust PID-PSS for an uncertain power system with simplified stability conditions.***Protection and Control of Modern Power System.* https://doi.org/10.1186/s41601-020-00165-9. (SCIE Indexed).
2. Lakshmi, A.S.V.V., Kumar, M.S. & Raju, M.R."**Optimal Robust PID-PSS Design for Melioration of Power System Stability Using Search and Rescue Algorithm**". *Journal of Control, Automation and Electrical Systems* (2021). https://doi.org/10.1007/s40313-021-00720-1. (Scopus Indexed**).**
3. Vijaya Lakshmi ASV, Siva Kumar M, Ramalinga Raju M. **"Control constraint based optimal PID-PSS design for a widespread operating power system using SAR algorithm"**. International Transactions on Electrical Energy Systems.(2021). e13146.https://doi: 10.1002/2050-7038.13146**.** (SCI Indexed)

CONFERENCES ATTENDED

1. Presented a paper by Vijaya Lakshmi, A.S.V., Mangipudi Siva Kumar, Manyala Ramalinga Raju with title "**Robust Stability Constraints for Optimal Lead lag PSS design using Interval Approach"** in International Conference on **Artificial Intelligence Techniques for Electrical Engineering Systems-2022** on May 6th-7th conducted by Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru.

B.Tech PROJECTS GUIDED:

1.Economic Load Dispatch Using Particle Swarm Optimization Method

2**.** Low Frequency oscillations damping of a SMIB using STATCOM along with conventional PSS

3**.** Application of STATCOM to increase Transient Stability of Wind farm
4.Modelling of UPFC for Enhancement of Power System Loadability and Stability
5.Design of Robust PI controller for plant with Parametric Uncertainty
6.Combined Economic Emission Dispatch problem for a Micro grid using JAYA algorithm.
7.Load frequency control of multi-area Power System using Jaya algorithm
8. Robust PI controller design for Interval Plant using Meta-Heuristic Algorithm
9. Optimal Robust PID design For Automatic Voltage Regulator
10.Development and design of Robust PID based AVR for enhancement of Voltage stability

PERSONAL DETAILS:

Name	Dr.A.S.V.Vijaya Lakshmi
Father's Name	A.Satyanarayana
Sex	Female
Marital status	Married
Date of Birth	25th May, 1981
Contact numbers	9849605392
Email	vijayalakshmi.asv@gmail.com
Languages known	English and Telugu.

I hereby declare that all the statements made above are correct to the best of my knowledge and belief. I also understand that any discrepancy found in the above information will render me liable for disqualification at any stage.

Place: Visakhapatnam **Yours sincerely,**

Date : 26/09/2022.

(Dr. A.S.V.Vijaya Lakshmi)